

Ian Galton

Education

Brown University , Providence, RI	ScB in Electrical Engineering	1984
California Institute of Technology , Pasadena, CA	MS/PhD in Electrical Engineering	1989/1992

Employment

Distinguished Professor of Electrical Engineering, University of California at San Diego	7/24 – present
Professor of Electrical Engineering, University of California at San Diego	7/02 – 6/24
Associate Professor of Electrical Engineering, University of California at San Diego	11/96 – 6/02
Assistant Professor of Electrical Engineering, University of California at Irvine	7/92 – 10/96
Design Engineer , Acuson, Inc., Mountain View, CA	8/86 – 9/88
Design Engineer , Mead Data Central, Menlo Park, CA	2/85 – 8/86
Design Engineer , Mindset Corporation, Sunnyvale, CA	7/84 – 1/85

UCSD Research Overview

Ian Galton manages the Integrated Signal Processing Group at the University of California, San Diego (UCSD). The group develops enabling technology for highly-integrated, high-performance, low-cost, communication systems. The research involves the invention, development, analysis, and proof-of-principle integrated circuit (IC) implementation of key communication system blocks such as data converters and frequency synthesizers. The emphases of the research are on the development of new digital signal processing techniques and new digital-like analog circuits which exploit the strengths of highly-scaled IC technology to mitigate the effects of non-ideal analog circuit behavior in mixed-signal and radio frequency ICs.

Selected Accomplishments, Activities, Honors, and Awards

- Holder of the “Skyworks Endowed Chair in High Performance Communications Devices and Circuits”
- Fellow of the Institute of Electrical and Electronics Engineers (IEEE)
- Recipient of the 2015-2016 and the 2016-2017 UCSD Electrical and Computer Engineering Department “Best Graduate Teacher” award
- Co-recipient of the 2015 IEEE International Solid-State Circuits Conference (ISSCC) Lewis Winner Award
- Recipient of the Brown Engineering Alumni Medal, 2012
- Co-recipient of the 2008 IEEE Transactions on Circuits and Systems Darlington Award
- Co-recipient of the 2008 IEEE ISSCC Jack Kilby Award
- Recipient of the 2005-2006 UCSD Electrical and Computer Engineering Department “Best Teacher Award”
- Recipient of the California Institute of Technology Charles Wilts PhD Thesis Prize, 1992
- Author or co-author of over 115 scientific papers in peer-reviewed academic journals and conferences
- Inventor or co-inventor on 25 U.S. Patents
- Member of the IEEE Solid-State Circuits Society Distinguished Lecturer Program, 2007-2010
- Organizer of the IEEE ISSCC Short Courses, 2007-2010
- Member of the IEEE ISSCC Technical Program Committee, 2005-2011
- IEEE Solid State Circuits Society Administrative Committee, 2002-2005
- IEEE Circuits and Systems Society Board of Governors, 1998-2001, 2002-2005
- Editor-in-Chief of the *IEEE Transactions on Circuits and Systems II: Analog to Digital Signal Processing*, 2001-2003
- Board of Directors of innoCOMM Wireless, San Diego, CA, through its 2001 purchase by National Semiconductor Corporation
- Technical consultant and/or Technical Advisory Board member for several semiconductor and communications companies
- Instructor in over 80 industry-oriented short courses on the design of data converters, PLLs, and wireless transceivers at IEEE conferences and through Mead Education