

# Ian Galton

## Education

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| <b>Brown University</b> , Providence, RI                 | <b>Sc.B.</b> in Electrical Engineering | June, 1984 |
| <b>California Institute of Technology</b> , Pasadena, CA | <b>M.S.</b> in Electrical Engineering  | June, 1989 |
| <b>California Institute of Technology</b> , Pasadena, CA | <b>Ph.D.</b> in Electrical Engineering | June, 1992 |

## Employment

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| Professor of Electrical Engineering, <b>University of California at San Diego</b> , La Jolla, CA           | 7/02 – present |
| Associate Professor of Electrical Engineering, <b>University of California at San Diego</b> , La Jolla, CA | 11/96 – 6/02   |
| Assistant Professor of Electrical Engineering, <b>University of California at Irvine</b> , Irvine, CA      | 7/92 – 10/96   |
| Design Engineer, <b>Acuson, Inc.</b> , Mountain View, CA   | 8/86 – 9/88    |
| Design Engineer, <b>Mead Data Central</b> , Menlo Park, CA   | 2/85 – 8/86    |
| Design Engineer, <b>Mindset Corporation</b> , Sunnyvale, CA  | 7/84 – 1/85    |

## UCSD Research Overview

Ian Galton manages the Integrated Signal Processing Group at the University of California, San Diego (UCSD). The group develops enabling technology for highly-integrated, high-performance, low-cost, communication systems. The research involves the invention, development, analysis, and proof-of-principle integrated circuit (IC) implementation of key communication system blocks such as data converters and frequency synthesizers. The emphases of the research are on the development of new digital signal processing techniques and new digital-like analog circuits that exploit the strengths of advanced IC technology to mitigate the effects of non-ideal analog circuit behavior in mixed-signal and radio frequency ICs. The techniques blur the traditionally-sharp analog-digital dividing lines in communication systems to overcome fundamental performance limitations of prior approaches.

## Selected Accomplishments, Activities, Honors, and Awards

- Author or co-author of ~100 scientific papers in peer-reviewed academic journals and conferences
- Inventor or co-inventor on ~22 U.S. Patents
- Fellow of the Institute of Electrical and Electronics Engineers (IEEE)
- Recipient of the 2015-2016 UCSD Electrical and Computer Engineering Department “Best Graduate Teacher” award
- Co-recipient of the 2015 IEEE International Solid-State Circuits Conference (ISSCC) Lewis Winner Award
- Recipient of the Brown Engineering Alumni Medal, 2012
- Co-recipient of the 2008 IEEE Transactions on Circuits and Systems Darlington Award
- Co-recipient of the 2008 IEEE ISSCC Jack Kilby Award
- Member of the IEEE Solid-State Circuits Society Distinguished Lecturer Program, 2007-2010
- Organizer of the IEEE ISSCC Short Courses, 2007-2010
- Member of the IEEE ISSCC Technical Program Committee, 2005-2011
- Recipient of the 2005-2006 UCSD Electrical and Computer Engineering Department “Best Teacher Award”
- IEEE Solid State Circuits Society Administrative Committee, 2002-2005
- IEEE Circuits and Systems Society Board of Governors, 1998-2001, 2002-2005
- Editor-in-Chief of the *IEEE Transactions on Circuits and Systems II: Analog to Digital Signal Processing*, 2001-2003
- Board of Directors of innoCOMM Wireless, San Diego, CA, through its 2001 purchase by National Semiconductor Corporation
- Technical consultant and/or Technical Advisory Board member for several semiconductor and communications companies
- Instructor in over 75 industry-oriented short courses on the design of data converters, PLLs, and wireless transceivers at IEEE conferences and through Mead Education
- Recipient of the California Institute of Technology Charles Wilts Ph.D. thesis Prize, 1992